

1961 Patents Office Drawing

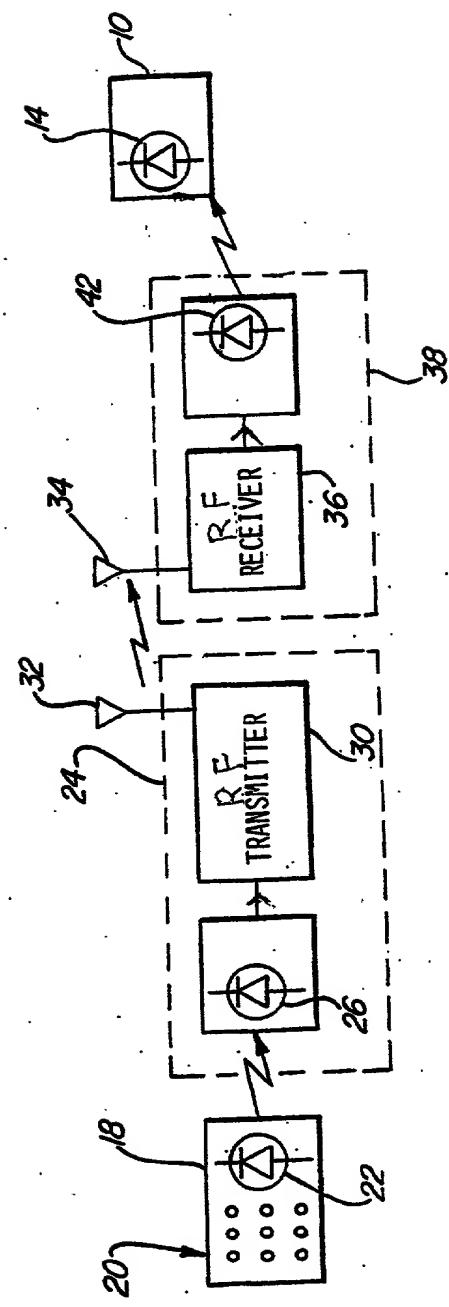


Fig. 1 A

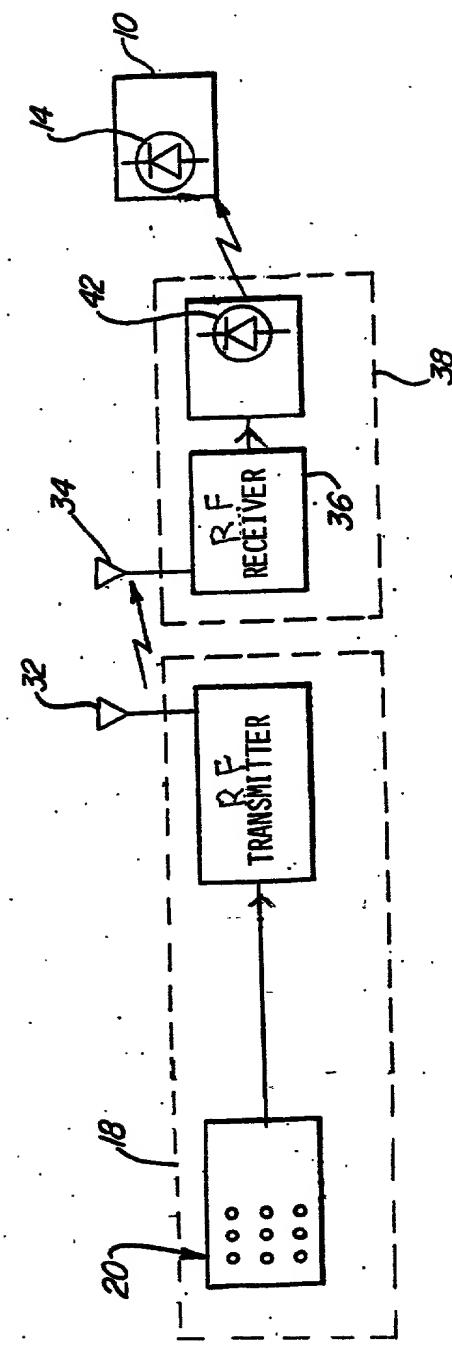
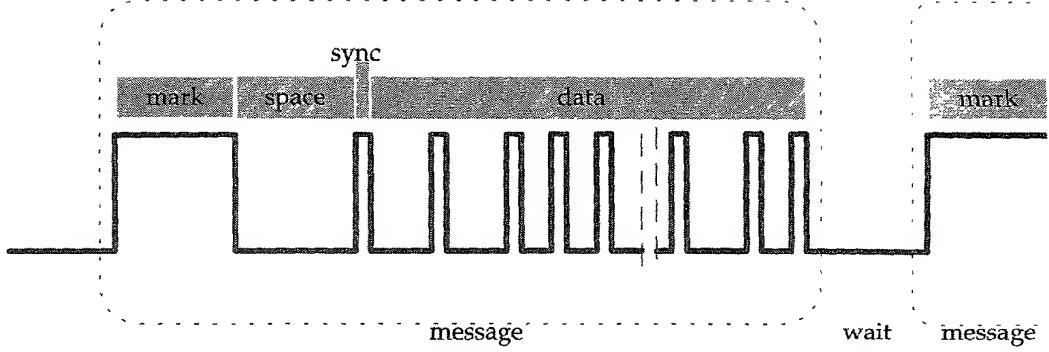
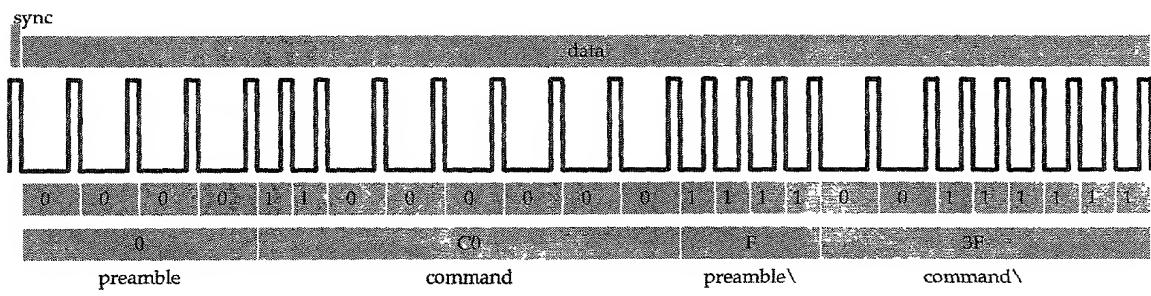


Fig. 1 B



F16.2



F16.3

mark space sync IR-CODE (4 bits) data message wait message

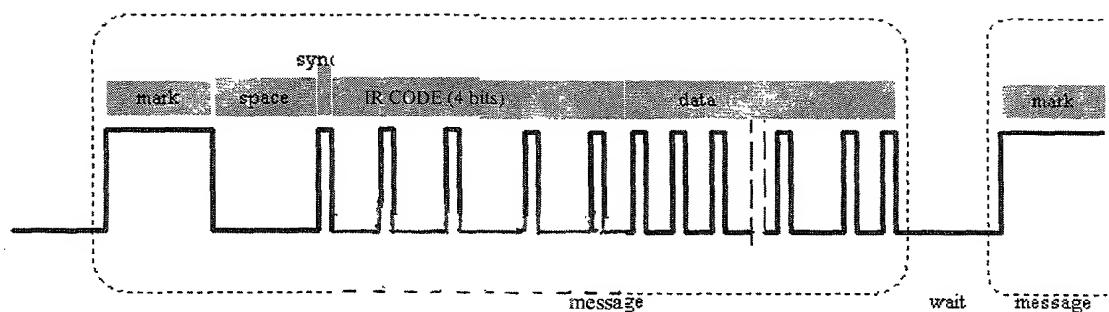


FIG. 4

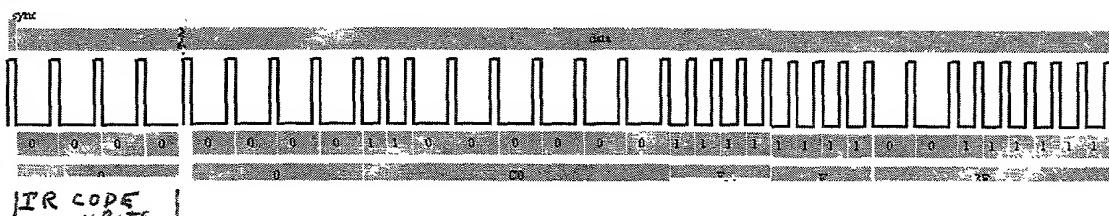


FIG. 5

